

between the one end of the second antenna pattern and the fourth point to form impedance matching for a high band in the second multi-band.

**17.** The electronic device of claim **15**, wherein the first antenna apparatus further comprises:

- a first capacitor circuit inserted into a transmission line connecting the first feed terminal to the first point, and the bypass conductor comprises a second capacitor circuit disposed between one point of the conductive border member between the second outer conductor and the fourth outer conductor and the ground to have a capacitance for bypassing the signal generated by the first antenna apparatus to the ground.

**18.** The electronic device of claim **17**, wherein the first antenna apparatus further comprises:

- a switch controlling a current path and a frequency band.

**19.** The electronic device of claim **11**, wherein the bypass conductor is further configured to bypass signals generated by the first antenna and the second antenna to the ground.

**20.** An electronic device, comprising:

- a first feed terminal connected to a circuit of a substrate of the electronic device;
- a second feed terminal connected to the circuit and electrically insulated from the first feed terminal;
- a conductive border member disposed along a periphery of the electronic device;
- a first antenna comprising a first switch configured to control the current path and the frequency band, and the first antenna being connected to the first feed terminal and the conductive border member;
- a second antenna comprising a second switch configured to control the current path and the frequency band, and the second antenna being connected to the second feed terminal and the conductive border member; and

- a bypass conductor configured to bypass interference signals generated by the first antenna and the second antenna to a ground of the substrate.

**21.** The electronic device of claim **20**, wherein the first antenna further comprises:

- a first outer conductor comprising a portion of the conductive border member extending from a first point connected to the first feed terminal and a first antenna pattern of the first antenna to a second point connected to the ground; and
- a second outer conductor comprising a portion of the conductive border member extending from a third point connected to a first bridge antenna pattern of the first antenna to a fourth point connected to the bypass path.

**22.** The electronic device of claim **21**, wherein the first switch is disposed on the substrate between the first point and the second point, and the first switch comprises a first terminal connected to the ground and a second terminal connected to the first outer conductor.

**23.** The electronic device of claim **20**, wherein the second antenna further comprises:

- a third outer conductor comprising a portion of the conductive border member extending from a fifth point connected to an end of a second bridge antenna pattern of the second antenna to a sixth point connected to the ground; and
- a fourth outer conductor comprising a portion of the conductive border member extending from the fifth point to a seventh point connected to the second feed terminal through a switch.

**24.** The electronic device of claim **23**, wherein the second switch is disposed on the substrate, and the second switch comprises a first terminal connected to the second feed terminal and a second terminal connected to the fourth outer conductor.

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